

Lech Jóźwiak

Biography

Lech Jóźwiak received his M. Sc. and Ph. D. degrees from the Faculty of Electronics, Warsaw University of Technology, Poland, in 1976 and 1982, correspondingly. From 1976 he has been continuously working in the area of his specialization in academia, research institutes or industry in Poland, The Netherlands, USA, Canada and Australia. Currently he is an Associate Professor, Head of the Section of Digital Circuits and Formal Design Methods, in the Department of Electronic Systems, Eindhoven University of Technology, The Netherlands. He is an author of the methodology of quality-driven system design, information-driven approach to digital circuit synthesis, and theories of information relationships and measures, and general decomposition of discrete relations that have both a theoretical value and considerable practical importance. He is also a creator of several practical products in the fields of embedded systems and EDA tools. He is an author of more than 180 journal and conference papers, some book chapters, and presenter of numerous keynotes and tutorials at international conferences and summer schools. He is an Editor in Chief of the journal of "Microprocessors and Microsystems", Director of EUROMICRO, Founder and Steering Committee Chair of the EUROMICRO Conference on Digital System Design, Advisory Committee and Organizing Committee member of the IEEE International Symposium on Quality Electronic Design; and program committee member of many other conferences. He is or was an advisor and consultant to the industry, Ministry of Economy, and Commission of the European Communities. He was a recipient of multiple Letters and Diplomas of Recognition for highly esteemed services and exceptional achievements from a. o. the Minister of Economy of Poland, presidents of professional societies and organizations (e.g. IEEE, EUROMICRO, ISQED, etc.) and chief editors of international scientific journals (e.g. IEEE Transactions on Computers; IEEE Transactions on CAD, etc.). In 2008 he was a recipient of the Honorary Fellow Award of the International Society of Quality Electronic Design for "Outstanding Achievements and Contributions to Quality of Electronic Design."